Strong Moding in Concurrent Logic/Constraint Programming

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Main source:

Ueda, K. and Morita, M., Moded Flat GHC and Its Message-Oriented Implementation Technique. *New Generation Computing*, Vol. 13, No. 1 (1994).

Introductory paper:

Ueda, K., I/O Mode Analysis in Concurrent Logic Programming. In *Theory and Practice of Parallel Programming*, LNCS 907, Springer, 1995.

Two Different Notions of Modes

 Modes for reasoning about temporal properties (time-of-call/exit instantiation states) of variables

e.g., Is X unbound when p(X) is called?

dependent on "computation rules"

2. Modes for reasoning about non-temporal properties

e.g., Which occurrence of X in the configuration p(X), q(X), r(X) may instantiate X eventually?

- independent of computation rules
- closer to a language construct

Abstract interpretation (mainly) deals with 1. Strong moding deals with 2.

Logical Variables and Communication

Observation: Logical variables are used for:

- 1. blackboard (competitive) communication (many writers, many readers), or
- 2. cooperative communication under established protocols:
 - (a) point-to-point communication(one writer, one reader),
 - (b) multicasting or broadcasting (one writer, many readers).
 - Failure is regarded as exception.

In both LP and Concurrent LP, it seems important to

- 1. distinguish between the two uses, and to
- 2. be able to infer the communication protocols for cooperative communication.

Strong moding provides compile-time support for "structured" communication.

Linear and Non-linear Clauses

A clauses is *linear* iff each variable occurs exactly twice in it.

Observation: Many clauses are linear in both LP and Concurrent LP —

```
append([],Y,Y).
append([A|X],Y,[A|Z]):- append(X,Y,Z).
```

though (of course) this is not always the case:

- Shared ground data
 p(...,X,...):-r(X),p(...,X,...).
- Wildcards
 p(_).
- Receive, check, and use
 p(...,X,...):-X>0,p(...,X,...).
- Nonlinear math
 p(X,Y) :- Y is X*X.

Concurrent Logic/Constraint Programming

Relational Language, Concurrent Prolog, Parlog, (Flat) GHC, KL1, FCP, Oc, Doc, Strand, Janus, AKL, Oz, ...

Communication mechanism: two interpretations

	algebraic	logical
representing information	substitutions	<i>constraints</i> (equality etc.)
receiving	matching	ask (entailment)
sending	unification	tell (publication)

Basic constructs for reactive concurrent programming seem to be converging to

ask + (eventual) tell .

Logical Variables as Communication Channels

Pros:

- *Natural* virtually no synchronization bugs.
- Simple and expressive
 - data- and demand-driven computation
 - incomplete messages with reply boxes
 - evolving process structures
 - (streams of)* streams
 - difference lists, ...
- A message sequence is a first-class object, not a special language construct.

Cons:

- Bidirectionality may lead to inefficiency.
- Variables are inadvertently used for noncooperative communication.

— "Unification failure" means that the store collapses.

Modes: An Electric Device Metaphor

Signal cables may have various structures (arrays of wires/pins). However,

- the two ends of a cable, viewed from outside, should have opposite polarity structures, and
- a plug and a socket should have opposite polarity structures when viewed from outside.



 $\begin{array}{rcl} \text{Goal} & \Longleftrightarrow & \text{Device} \\ \text{Variable} & \Longleftrightarrow & \text{Cable} \end{array}$

Strong Moding

Deals with the *dynamic* (but implementationindependent) properties of programs *statically*:

- dataflow aspect (protocols)
- resource aspect

Shares advantages with strong typing:

- 1. helps programmers understand their programs better
- 2. compile-time detection of mode/type errors
- 3. compile-time establishment of fundamental properties
- 4. basic information for program optimization
- 5. encourages modular programming

Possible problems:

- explicit moding is burdensome
- monomorphism is too restrictive

Mode Inference: The Idea

```
merge([],Y,Z):-Z=Y.
merge(X,[],Z):-Z=X.
merge([A|X],Y,ZO):-ZO=[A|Z],merge(X,Y,Z).
merge(X,[A|Y],ZO):-ZO=[A|Z],merge(X,Y,Z).
```

We want to design a simple set of rules that allows us to infer properties such as:

- the 1st and 2nd arguments are input streams,
- the 3rd argument is an output stream, and
- the communication protocols used by these streams are identical.

Requirements:

- Retain the expressive power of the language.
- Allow efficient analysis.
- Allow separate analysis.

Syntax of a Subset of Flat GHC (or Oc)

(program)
$$P ::=$$
 set of R 's
(program clause) $R ::= A := B$
(body) $B ::=$ multiset of G 's
(goal) $G ::= T_1 = T_2 | A$
(atom) $A ::= p(T_1, \dots, T_n), \quad p \neq `=`$
(term) $T ::=$ (as in first-order logic)
(goal clause) $Q ::= :- B$

For simplicity, we ignore

- guard goals and
- non-linear heads (i.e., non-left-linear clauses).

Operational Semantics

Configuration: $\langle B, C, V \rangle$ such that $\mathcal{V}_B \cup \mathcal{V}_C \subseteq V$ (\mathcal{V}_F : set of all variables in F)

The execution of :- B_0 starts with $\langle B_0, \emptyset, \mathcal{V}_{B_0} \rangle$.

$$\frac{\mathcal{P} \vdash \langle B_1, C, V \rangle \longrightarrow \langle B'_1, C', V' \rangle}{\mathcal{P} \vdash \langle B_1 \cup B_2, C, V \rangle \longrightarrow \langle B'_1 \cup B_2, C', V' \rangle}$$

 $\overline{\mathcal{P} \vdash \langle \{t_1 = t_2\}, C, V \rangle \longrightarrow \langle \emptyset, C \cup \{t_1 = t_2\}, V \rangle}$

$$\{h: -B\} \cup \mathcal{P} \vdash \\ \langle \{b\}, C, V \rangle \longrightarrow \langle B, C \cup \{\overline{b} = \overline{h}\}, (V \cup \mathcal{V}_{h:-B}) \rangle \\ \left(\begin{array}{c} \text{if } \mathcal{E} \models \forall (C \Rightarrow \exists \mathcal{V}_{h}(\overline{b} = \overline{h})) \\ \text{and } \mathcal{V}_{h:-B} \cap V = \emptyset \end{array} \right)$$

The Mode System of Moded Flat GHC

• The mode system assigns polarity *structures* to predicate arguments

— so that each part of data structures will be determined cooperatively, namely by *ex*-*actly one* process.

- Admits mode *inference* as well as mode *dec-laration* + mode *checking*.
- Constraint-based.
- Decidable and efficient constraints can be solved (mostly) as unification over feature graphs.

The Mode System of Moded Flat GHC

A mode tells which process will determine which parts of data structures, or the dataflow aspect of communication protocols.

The 'parts' are specified by *paths* (= string of $\langle symbol, arg \rangle$ pairs).

db([update(3,a),search(5,X)|...]) \rightarrow X occurs at the path

 $\langle \texttt{db}, 1 \rangle \langle \textbf{.}, 2 \rangle \langle \textbf{.}, 1 \rangle \langle \texttt{search}, 2 \rangle.$

A set of program/goal clauses is *well-moded* if it satisfies all the mode constraints imposed by individual clauses.

 \rightarrow Mode analysis

- = constraint solving (in general)
- unification over feature graphs (in practice)

The Mode System: Definition

- Pred/Fun/Var: set of pred./func./variable symbols
- *Term/Atom*: set of terms/atoms over *Pred*, *Fun*, *Var*
- $N_p \stackrel{\text{def}}{=} \{1, 2, \dots, n_p\}$, for each n_p -ary $p \in Pred$
- $N_f \stackrel{\text{def}}{=} \{1, 2, \dots, n_f\}$, for each n_f -ary $f \in Fun$
- $P_{Atom} \stackrel{\text{def}}{=} \left(\sum_{p \in Pred} N_p\right) \times \left(\sum_{f \in Fun} N_f\right)^*$

typical element: $\langle p,i\rangle\langle f_1,j_1\rangle\ldots\langle f_n,j_n\rangle$

- $\tilde{a}: P_{Atom} \to Var \cup Fun \cup \{\bot\}$, for each $a \in Atom$ returns the symbol at the given path (or \bot).
- P_{Term} and \tilde{t} ($t \in Term$) are defined similarly.

Modes as functions

- $M \stackrel{\mathsf{def}}{=} P_{Atom} \to \{in, out\}$
- cf. definition of infinite trees

The Mode System (Continued)

$$m: M \ (= P_{Atom} \to \{in, out\})$$
$$p \in P_{Atom}, \quad q \in P_{Term}$$

Submodes

- $m/p: P_{Term} \to \{in, out\}, \quad q \mapsto m(pq)$
- $(m/p)/q \stackrel{\text{def}}{=} m/(pq)$

Mode inversion

- $\overline{in} \stackrel{\text{def}}{=} out$, $\overline{out} \stackrel{\text{def}}{=} in$
- $\overline{m}: M, \quad p \mapsto \overline{m(p)}$
- $\bullet \ \overline{m/p} \stackrel{\mathrm{def}}{=} \overline{m}/p$

Constant submodes

- $IN: P_{Term} \to \{in, out\}, \quad q \mapsto in$
- $OUT \stackrel{\mathsf{def}}{=} \overline{IN}$

Mode Analysis

Purpose: To find a *well-moding* m : M which satisfies all the mode constraints syntactically imposed by the (program, goal) pair.

Constraints imposed by a clause h :- B:

(HF) $\tilde{h}(p) \in Fun \Rightarrow m(p) = in$ (BU) $(t_1 = k t_2) \in B \Rightarrow m/\langle =_k, 1 \rangle = \overline{m/\langle =_k, 2 \rangle}$ (BF) $a \in B \land \tilde{a}(p) \in Fun \Rightarrow m(p) = in$ (BV) $v \in Var$ occurs $n (\geq 1)$ times in h and B at p_1, \ldots, p_n , of which the occurrences in h are at p_1, \ldots, p_k $(k \geq 0)$

$$\Rightarrow \begin{cases} \mathcal{R}(\{\frac{m/p_1}{m, \dots, m/p_n}\}), & k = 0; \\ \mathcal{R}(\{\frac{m/p_1}{m, m/p_{k+1}, \dots, m/p_n}\}), & k > 0; \end{cases}$$

where \mathcal{R} is a 'cooperativeness' relation:

$$\mathcal{R}(S) \stackrel{\text{def}}{=} \forall q \in P_{Term} \exists s \in S$$
$$(s(q) = out \land \forall s' \in S \setminus \{s\} (s'(q) = in))$$

Principles Behind the Constraints

A Variable is a Cable ...





... Or a Hub.



 $\mathcal{R}(\{s_0,s_1,s_2,s_3\})$



$$s_1 = \overline{s_2}$$

Constraint for Connection

Principles Behind the Constraints

Clause heads and body goals have inverse polarities, so do their arguments.



Goal-head connection



 $\mathcal{R}(\{\overline{s}\} \cup S_1) \land \mathcal{R}(\{s\} \cup S_2) \Rightarrow \mathcal{R}(S_1 \cup S_2)$



How Mode Analysis Works

merge([],Y,Z):- $Z=_1Y$. merge(X,[],Z):- $Z=_2X$. merge([A|X],Y,ZO):- $ZO=_3[A|Z]$, merge(X,Y,Z). merge(X,[A|Y],ZO):- $ZO=_4[A|Z]$, merge(X,Y,Z).

From the third clause:

How Mode Analysis Works

merge([],Y,Z):- $Z=_1Y$. merge(X,[],Z):- $Z=_2X$. merge([A|X],Y,ZO):- $ZO=_3[A|Z]$, merge(X,Y,Z). merge(X,[A|Y],ZO):- $ZO=_4[A|Z]$, merge(X,Y,Z).

Number of constraints generated: 24

 $(m(p) = in: 6, m/p_1 = m/p_2: 12, m/p_1 = \overline{m/p_2}: 6)$

By eliminating constraints on $=_k$, we obtain

$$egin{aligned} m(\langle {\tt merge},1
angle) &= in\ m/\langle {\tt merge},1
angle\langle .,2
angle &= m/\langle {\tt merge},1
angle\ m/\langle {\tt merge},2
angle &= m/\langle {\tt merge},1
angle\ m/\langle {\tt merge},3
angle &= \overline{m/\langle {\tt merge},1
angle} \end{aligned}$$

How to deal with them efficiently?

Mode Graphs and Principal Mode Schemes

A set of mode constraints forms a "*principal mode scheme*" that can best be expressed as a *mode graph*.





A Stack & Driver Example



..., stack(S, none), drive(10,S), ...





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Difference Lists



Mutual Recursion

driver(Fs,IOs0): IOs0=[gett(X)|IOs1], checkinput(Fs,IOs1,X).
checkinput(Fs, IOs, done):-Fs=[],IOs=[].
checkinput(Fs0,IOs0,more): Fs0=[N|Fs1],IOs0=[putt(N),n1|IOs1],
 driver(Fs1,IOs1).



In Prolog, arg (and destructive set_arg) provides array access functionalities.

Principle: For built-in predicates, consider the mode constraints imposed by their virtual definitions.

Under the mode system, the *basic* access operation should be:



Moral: Values are resources. Array elements should be *removed* when accessed.

Arrays

In Prolog, functor(-,+,+) initializes the arguments of the created structure with distinct fresh variables, which are *instantiated* if necessary.

Under the mode system, the arguments should be initialized by constants, which are *updated* by $set_arg/5$.

 The update is in-place if the array occurs at a single-reference path.

Other generic array operations:

- swap
- split
- concatenate
- change-shape (for multidimensional arrays),

. . .

A singleton variable may well be a misspelled variable.

cf. a "dangling" cable and an unplugged socket

$$\begin{split} \mathbf{p}(\mathbf{X}\mathbf{0}, \ldots) &:= \ldots, \mathbf{p}(\mathbf{X}\mathbf{0}, \ldots) \\ &\to m/\langle \mathbf{p}, \mathbf{1} \rangle = IN \wedge m/\langle \mathbf{p}, \mathbf{1} \rangle = OUT \\ &\to \text{mode error} \end{split}$$

Note: Not all singleton variables indicate errors.

length([], NO,N):-N:=NO. length([_|L],NO,N):-N1:=NO+1,length(L,N1,N).

— length will be used as a "byway" process without affecting the protocol of the rest of the processes.



Well-Moded Programs Do Not Go Wrong

P: a program*B*: (body of) a goal clause*m*: a mode

Lemma 1

If B : m and B contains $t_1 =_k t_2$, at least one of t_1 and t_2 is a variable.

Theorem 1*

If P : m, B : m and $P \vdash \langle B, \emptyset, V \rangle \longrightarrow \langle B', C', V' \rangle$, then C' is consistent and $B' \cdot C' : m$.

 $(B' \cdot C': B' \text{ instantiated by } C')$

Corollary 1*

If P : m, B : m and $P \vdash \langle B, \emptyset, V \rangle \xrightarrow{*} \langle B', C', V' \rangle$, then C' is consistent and $B' \cdot C' : m$.

*Holds unless the extended occur check (which excludes unification of the form v=v) fails.

Extended Occur Check

A goal of the form v = v creates a meaningless short-circuit.

The pair

$$G: := p(A,A), q(A), r(A).$$

P: p(X,Y) := X = Y.

imposes $m/\langle {\bf q}, {\bf 1} \rangle = m/\langle {\bf r}, {\bf 1} \rangle = IN$, while the reduced goal

G': :-q(A),r(A).

imposes $m/\langle q, 1 \rangle = \overline{m/\langle r, 1 \rangle}$, which would violate Theorem 1.

Groundness Property Follows from the Termination Property

Theorem 2

Suppose P : m, B : m, and B has succeeded under the extended occur check. Then for each v in B, a unification goal $\overline{v} = t$ or $t = \overline{v}$ must have been executed.

Corollary 2

The final store maps all the variables in B to ground terms.

Cost of the Mode Analysis

- The number of constraints imposed: O(n) (∀-quantified or non-quantified), where n is the size of the program
- Adding one unary/binary constraint
 - \approx unification of a feature graph with another small feature graph
 - \approx merging of top-level features + merging of submode graphs
 - → $O(d \cdot \alpha(n))$ time, where d: size of the subgraph to be unified ('complexity' of data structures), α : inverse of the Ackermann function
- Total cost:

 $O(nd \cdot \alpha(n))$ for all-at-once analysis $O(n \log n + nd \cdot \alpha(n))$ for separate analysis

Non-Unary/Binary Constraints

- Imposed by non-linear clauses.
- Cannot be represented by mode graphs.
- Should be delayed many of n(> 2)-ary constraints will be reduced to unary/binary ones by other constraints.

p(X, ...) := r(X, ...), p(X, ...) $\rightarrow m/\langle r, 1 \rangle = IN,$ $m/\langle p, 1 \rangle = (unconstrained)$

- Some constraints may remain unreduced, whose satisfiability must be checked eventually.
- The practical solution is to let programmers declare the modes of the paths where non-linear variables occur.

Implications to Programming Style

- 1. Advocates the "programming as wiring" paradigm, or (equivalently) programming with linear clauses.
 - leads to more generic mode schemes
 - encourages "structured dataflow"
 - less error-prone
- 2. Encourages graceful termination.

— A process cannot discard its arguments upon termination if it contains variables to instantiate. (cf. Corollary 2)

(e.g., an output stream must be terminated by [].)

 All streams will be closed upon termination.

Path-Based Program Analysis

Path-based analysis can be used also for

- the distinction between one-to-one and possibly one-to-many communication
 — which paths are 'shared' paths?
- type systems
 - which paths are used for the data obeyed by the constraint system C?
 - what function symbols may appear at this path?

Resources received at linear (non-shared) paths in a clause head can be

- reclaimed (compile-time garbage collection) if not passed to the body, or
- locally recycled to represent new data used in the body,

without any run-time checking (e.g., reference counting).

Resource-Conscious Programming

Insertion sort example:

```
sort([], S) :- S=[].
sort([X|L0],S) :-
    sort(L0,S0), insert(X,S0,S).
insert(X,[], R) :- R=[X].
insert(X,[Y|L], R) :- X=<Y | R=[X,Y|L].
insert(X,[Y|L0],R) :- X>Y |
    R=[Y|L], insert(X,L0,L).
```

By slight modification, becomes *linear w.r.t. data resources*:

```
sort([], S) :- S=[].
sort([X|L0],S) :-
    sort(L0,S0), insert([X],S0,S).
insert([X],[], R) :- R=[X].
insert([X],[Y|L], R) :- X=<Y | R=[X,Y|L].
insert([X],[Y|L0],R) :- X>Y |
    R=[Y|L], insert([X],L0,L).
```

Extension: Polymorphic Modes

A polymorphic predicate is allowed to have different modes for each call.

Typical example: '=' (unification)

For polymorphic predicates,

- compute their principal mode schemes (i.e., mode graphs), and
- allow different calls to have different instantiations of them. (e.g., merge₁, merge₂, ...)

This can be implemented by making a *copy* of the mode graph for each call, rather than the original graph.

(cf. ML: $(\lambda x.A)E$ vs. let x = E in A)

Extension: Higher-Order

call is just a predicate with the constraint $m/\langle \text{call}, 1 \rangle = m$ (by confusing pred. and func. symbols).

apply needs extension.

twice(P,X,Z):- apply(P,X,Y), apply(P,Y,Z).

Whether P is

- a predicate symbol,
- a list of clauses (ground representation), or
- a compiled code with mode information,

it is a ground term at the first-order, but must have a predicate mode as well. The moding in the monomorphic case would be:



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Extension: Non-Herbrand Constraint Systems

- Rational terms Immediate.
- Numerical constraints Can be moded if dataflow can be determined statically.
- Equational theories

Associativity and commutativity can be included naturally (they preserve resources).
 Idempotency involves resource contraction/copying.

Example: Bags (= multisets) enjoy

 $t_1 \cup t_2 = t_2 \cup t_1$ and $t_1 \cup (t_2 \cup t_3) = (t_1 \cup t_2) \cup t_3$. So the paths for bags should obey the constraint:



Constraint-Based Program Analysis

 Abstract interpretation usually computes fixpoints by iteration, while constraint-based analysis computes fixpoints by unification (or constraint solving)

 — on the assumption that a single iteration should lead to a fixpoint.

- Constraint-based analysis provides unified treatment of
 - declaration constraints provided by a programmer,
 - checking consistency checking between constraints from the program and those given by programmers,
 - *inference* constraint solving.
- Incremental inherently amenable to separate analysis.

Related Work and Future

Languages

- Strand (Foster and Taylor)
- Doc (Hirata)
- A'UM (Yoshida et al.)
- Moded Flat GHC (Ueda et al.)
- Janus (Saraswat et al.)

Implementation of the Mode System

- MGTP-based (Koshimura et al.)
- Mode graph (Ueda)
- Another mode graph (Tick et al.)

Applications and Future Work

- Message-oriented Implementation (sequential and parallel) (Ueda et al.)
- Optimized distributed unification
- Style checker
- High-performance computing

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