## Strong Moding in

## Concurrent Logic/Constraint Programming

## Kazunori Ueda

Dept. of Information and
Computer Science
Waseda University

Main source:
Ueda, K. and Morita, M., Moded Flat GHC and Its Message-Oriented Implementation Technique. New Generation Computing, Vol. 13, No. 1 (1994).

Introductory paper: Ueda, K., I/O Mode Analysis in Concurrent Logic Programming. In Theory and Practice of Parallel Programming, LNCS 907, Springer, 1995.

## Two Different Notions of Modes

1. Modes for reasoning about temporal properties (time-of-call/exit instantiation states) of variables

## e.g., Is $X$ unbound when $p(X)$ is called?

- dependent on "computation rules"

2. Modes for reasoning about non-temporal properties
e.g., Which occurrence of $x$ in the configuration $p(X), q(X), r(X)$ may instantiate $X$ eventually?

- independent of computation rules
- closer to a language construct

Abstract interpretation (mainly) deals with 1. Strong moding deals with 2.

## Logical Variables and Communication

Observation: Logical variables are used for:

1. blackboard (competitive) communication (many writers, many readers), or
2. cooperative communication under established protocols:
(a) point-to-point communication (one writer, one reader),
(b) multicasting or broadcasting (one writer, many readers).

- Failure is regarded as exception.

In both LP and Concurrent LP, it seems important to

1. distinguish between the two uses, and to
2. be able to infer the communication protocols for cooperative communication.

Strong moding provides compile-time support for "structured" communication.

## Linear and Non-linear Clauses

A clauses is linear iff each variable occurs exactly twice in it.

Observation: Many clauses are linear in both LP and Concurrent LP -

```
append([],Y,Y).
append([A|X],Y,[A|Z]):- append(X,Y,Z).
```

- though (of course) this is not always the case:
- Shared ground data

$$
p(\ldots, X, \ldots):-r(X), p(\ldots, X, \ldots) .
$$

- Wildcards

$$
p\left(\left(_{-}\right) .\right.
$$

- Receive, check, and use

$$
p(\ldots, X, \ldots):-X>0, p(\ldots, X, \ldots) .
$$

- Nonlinear math

$$
\mathrm{p}(\mathrm{X}, \mathrm{Y}):-\mathrm{Y} \text { is } \mathrm{X} * \mathrm{X} .
$$

## Concurrent Logic/Constraint Programming

Relational Language, Concurrent Prolog, Parlog, (Flat) GHC, KL1, FCP, Oc, Doc, Strand, Janus, AKL, Oz, ...

Communication mechanism: two interpretations

|  | algebraic | logical |
| :---: | :---: | :---: |
| representing <br> information <br> receiving | substitutions | matching |
| (equality etc.) | ask |  |
| (entailment) |  |  |
| sending | unification | tell |
| (publication) |  |  |

Basic constructs for reactive concurrent programming seem to be converging to
ask + (eventual) tell

## Logical Variables as Communication Channels

## Pros:

- Natural - virtually no synchronization bugs.
- Simple and expressive
- data- and demand-driven computation
- incomplete messages with reply boxes
- evolving process structures
- (streams of)* streams
- difference lists, ...
- A message sequence is a first-class object, not a special language construct.


## Cons:

- Bidirectionality may lead to inefficiency.
- Variables are inadvertently used for noncooperative communication.
- "Unification failure" means that the store collapses.


## Modes: An Electric Device Metaphor

Signal cables may have various structures (arrays of wires/pins). However,

- the two ends of a cable, viewed from outside, should have opposite polarity structures, and
- a plug and a socket should have opposite polarity structures when viewed from outside.

$\begin{aligned} \text { Goal } & \Longleftrightarrow \text { Device } \\ \text { Variable } & \Longleftrightarrow \text { Cable }\end{aligned}$


## Strong Moding

Deals with the dynamic (but implementationindependent) properties of programs statically:

- dataflow aspect (protocols)
- resource aspect

Shares advantages with strong typing:

1. helps programmers understand their programs better
2. compile-time detection of mode/type errors
3. compile-time establishment of fundamental properties
4. basic information for program optimization
5. encourages modular programming

Possible problems:

- explicit moding is burdensome
- monomorphism is too restrictive


## Mode Inference: The Idea

$$
\begin{aligned}
& \operatorname{merge}([], Y, Z):-Z=Y \\
& \operatorname{merge}(X,[], Z):-Z=X . \\
& \operatorname{merge}([A \mid X], Y, Z O):-Z 0=[A \mid Z], \operatorname{merge}(X, Y, Z) . \\
& \operatorname{merge}(X,[A \mid Y], Z O):-Z 0=[A \mid Z], \operatorname{merge}(X, Y, Z) .
\end{aligned}
$$

We want to design a simple set of rules that allows us to infer properties such as:

- the 1st and $2 n d$ arguments are input streams,
- the 3rd argument is an output stream, and
- the communication protocols used by these streams are identical.

Requirements:

- Retain the expressive power of the language.
- Allow efficient analysis.
- Allow separate analysis.


## Syntax of a Subset of Flat GHC (or Oc)

(program) $P::=$ set of $R$ 's
(program clause) $R::=A:-B$
(body) $B::=$ multiset of $G$ 's
(goal) $G::=T_{1}=T_{2} \quad \mid \quad A$
(atom) $A::=p\left(T_{1}, \ldots, T_{n}\right), \quad p \neq{ }^{\prime}=’$
(term) $T::=$ (as in first-order logic)
(goal clause) $Q::=:-B$

For simplicity, we ignore

- guard goals and
- non-linear heads (i.e., non-left-linear clauses).


## Operational Semantics

Configuration: $\langle B, C, V\rangle$ such that $\mathcal{V}_{B} \cup \mathcal{V}_{C} \subseteq V$ ( $\mathcal{V}_{F}$ : set of all variables in $F$ )

The execution of :- $B_{0}$ starts with $\left\langle B_{0}, \emptyset, \mathcal{V}_{B_{0}}\right\rangle$.

$$
\frac{\mathcal{P} \vdash\left\langle B_{1}, C, V\right\rangle \longrightarrow\left\langle B_{1}^{\prime}, C^{\prime}, V^{\prime}\right\rangle}{\mathcal{P} \vdash\left\langle B_{1} \cup B_{2}, C, V\right\rangle \longrightarrow\left\langle B_{1}^{\prime} \cup B_{2}, C^{\prime}, V^{\prime}\right\rangle}
$$

$$
\overline{\mathcal{P} \vdash\left\langle\left\{t_{1}=t_{2}\right\}, C, V\right\rangle \longrightarrow\left\langle\emptyset, C \cup\left\{t_{1}=t_{2}\right\}, V\right\rangle}
$$

$$
\begin{aligned}
& \{h:-B\} \cup \mathcal{P} \vdash \\
& \quad\{\{b\}, C, V\rangle \longrightarrow\left\langle B, C \cup\{\bar{b}=\bar{h}\},\left(V \cup \mathcal{V}_{h:-B}\right)\right\rangle \\
& \qquad\binom{\text { if } \mathcal{E} \models \forall\left(C \Rightarrow \exists \mathcal{V}_{h}(\bar{b}=\bar{h})\right)}{\text { and } \mathcal{V}_{h:-B} \cap V=\emptyset}
\end{aligned}
$$

## The Mode System of Moded Flat GHC

- The mode system assigns polarity structures to predicate arguments
- so that each part of data structures will be determined cooperatively, namely by exactly one process.
- Admits mode inference as well as mode declaration + mode checking.
- Constraint-based.
- Decidable and efficient - constraints can be solved (mostly) as unification over feature graphs.


## The Mode System of Moded Flat GHC

A mode tells which process will determine which parts of data structures, or the dataflow aspect of communication protocols.

The 'parts' are specified by paths (= string of〈symbol, arg〉 pairs).

```
db ([update (3, a), search (5,X)|...])
```

$\rightarrow \mathrm{X}$ occurs at the path

$$
\langle\mathrm{db}, 1\rangle\langle., 2\rangle\langle., 1\rangle\langle\text { search, } 2\rangle .
$$

A set of program/goal clauses is well-moded if it satisfies all the mode constraints imposed by individual clauses.
$\rightarrow$ Mode analysis
$=$ constraint solving (in general)
$=$ unification over feature graphs (in practice)

## The Mode System: Definition

- Pred/Fun/Var: set of pred./func./variable symbols
- Term/Atom: set of terms/atoms over Pred, Fun, Var
- $N_{p} \stackrel{\text { def }}{=}\left\{1,2, \ldots, n_{p}\right\}$, for each $n_{p}$-ary $p \in \operatorname{Pred}$
- $N_{f} \stackrel{\text { def }}{=}\left\{1,2, \ldots, n_{f}\right\}$, for each $n_{f}$-ary $f \in$ Fun
- $P_{\text {Atom }} \stackrel{\text { def }}{=}\left(\sum_{p \in \text { Pred }} N_{p}\right) \times\left(\sum_{f \in F u n} N_{f}\right)^{*}$
typical element: $\langle p, i\rangle\left\langle f_{1}, j_{1}\right\rangle \ldots\left\langle f_{n}, j_{n}\right\rangle$
- $\tilde{a}: P_{\text {Atom }} \rightarrow \operatorname{Var} \cup F u n \cup\{\perp\}$, for each $a \in$ Atom returns the symbol at the given path (or $\perp$ ).
- $P_{\text {Term }}$ and $\tilde{t}(t \in$ Term $)$ are defined similarly.


## Modes as functions

- $M \stackrel{\text { def }}{=} P_{\text {Atom }} \rightarrow\{$ in, out $\}$
cf. definition of infinite trees


## The Mode System (Continued)

$m: M\left(=P_{\text {Atom }} \rightarrow\{\right.$ in, out $\left.\}\right)$
$p \in P_{\text {Atom }}, \quad q \in P_{\text {Term }}$

## Submodes

- $m / p: P_{\text {Term }} \rightarrow\{$ in, out $\}, \quad q \mapsto m(p q)$
- $(m / p) / q \stackrel{\text { def }}{=} m /(p q)$


## Mode inversion

- $\overline{\text { in }} \xlongequal{\text { def }}$ out, $\overline{o u t} \stackrel{\text { def }}{=}$ in
- $\bar{m}: M, \quad p \mapsto \overline{m(p)}$
- $\overline{m / p} \stackrel{\text { def }}{=} \bar{m} / p$


## Constant submodes

- IN : $P_{\text {Term }} \rightarrow\{$ in, out $\}, \quad q \mapsto i n$
- $O U T \stackrel{\text { def }}{=} \overline{I N}$


## Mode Analysis

Purpose: To find a well-moding $m$ : $M$ which satisfies all the mode constraints syntactically imposed by the (program, goal) pair.

Constraints imposed by a clause $h:-B$ :
(HF) $\widetilde{h}(p) \in F u n \Rightarrow m(p)=i n$
( BU$)\left(t_{1}={ }_{k} t_{2}\right) \in B \Rightarrow m /\left\langle={ }_{k}, 1\right\rangle=\overline{m /\left\langle{ }_{k}, 2\right\rangle}$
(BF) $a \in B \wedge \tilde{a}(p) \in F u n \Rightarrow m(p)=i n$
(BV) $v \in \operatorname{Var}$ occurs $n(\geq 1)$ times in $h$ and $B$ at $p_{1}, \ldots, p_{n}$, of which the occurrences in $h$ are at $p_{1}, \ldots, p_{k}(k \geq 0)$
$\Rightarrow \begin{cases}\mathcal{R}\left(\left\{m / p_{1}, \ldots, m / p_{n}\right\}\right), & k=0 ; \\ \mathcal{R}\left(\left\{m / p_{1}, m / p_{k+1}, \ldots, m / p_{n}\right\}\right), & k>0 ;\end{cases}$
where $\mathcal{R}$ is a 'cooperativeness' relation:
$\mathcal{R}(S) \stackrel{\text { def }}{=} \forall q \in P_{\text {Term }} \exists s \in S$
$\left(s(q)=\right.$ out $\wedge \forall s^{\prime} \in S \backslash\{s\}\left(s^{\prime}(q)=\right.$ in $\left.)\right)$

## Principles Behind the Constraints

## A Variable is a Cable ...



$$
\mathcal{R}\left(\left\{s_{1}, s_{2}\right\}\right) \Leftrightarrow s_{1}=\overline{s_{2}}
$$

... Or a Hub.

$\mathcal{R}\left(\left\{s_{0}, s_{1}, s_{2}, s_{3}\right\}\right)$

$s_{1}=\overline{s_{2}}$
Constraint for
Connection

## Principles Behind the Constraints

Clause heads and body goals have inverse polarities, so do their arguments.


Goal-head connection
(HF)
(BF)
(BU)


## Resolution Principle

$$
\mathcal{R}\left(\{\bar{s}\} \cup S_{1}\right) \wedge \mathcal{R}\left(\{s\} \cup S_{2}\right) \Rightarrow \mathcal{R}\left(S_{1} \cup S_{2}\right)
$$



## How Mode Analysis Works

$$
\begin{aligned}
& \operatorname{merge}([], Y, Z):-Z={ }_{1} Y . \\
& \operatorname{merge}(X,[], Z):-Z={ }_{2} X . \\
& \operatorname{merge}([A \mid X], Y, Z O):-Z 0={ }_{3}[A \mid Z], \operatorname{merge}(X, Y, Z) . \\
& \operatorname{merge}(X,[A \mid Y], Z O):-Z 0={ }_{4}[A \mid Z], \operatorname{merge}(X, Y, Z) .
\end{aligned}
$$

From the third clause:
$m(\langle$ merge, 1$\rangle)=$ in by (HF) applied to "."
$m /\langle=3,1\rangle=m /\langle=3,2\rangle$
$m(\langle=3,2\rangle)=i n$
by (BU) applied to =3
by (BF) applied to "." $m /\langle$ merge, 1$\rangle\langle., 1\rangle=m /\langle=3,2\rangle\langle., 1\rangle$
by (BV) applied to A $m /\langle$ merge, 1$\rangle\langle., 2\rangle=m /$ merge, 1$\rangle$
by (BV) applied to X $m /\langle$ merge, 2$\rangle=m /\langle$ merge, 2$\rangle$ by (BV) applied to $Y$ $m /\langle$ merge, 3$\rangle=m /\langle=3,1\rangle$ by (BV) applied to $\mathrm{Z0}$ $m /\langle=3,2\rangle\langle., 2\rangle=\overline{m /\langle\text { merge }, 3\rangle}$
by (BV) applied to $Z$

## How Mode Analysis Works

```
merge([],Y,Z):- Z= 1Y.
merge(X,[],Z):- Z= 2X.
merge([A|X],Y,ZO):- Z0=3[A|Z], merge(X,Y,Z).
merge(X,[A|Y],ZO):- ZO=4 [A|Z], merge(X,Y,Z).
```

Number of constraints generated: 24
$\left(m(p)=\right.$ in: 6, $m / p_{1}=m / p_{2}: 12$, $\left.m / p_{1}=\overline{m / p_{2}}: 6\right)$

By eliminating constraints on $=_{k}$, we obtain

$$
\begin{aligned}
m(\langle\text { merge }, 1\rangle) & =\text { in } \\
m /\langle\text { merge }, 1\rangle\langle., 2\rangle & =m /\langle\text { merge }, 1\rangle \\
m /\langle\text { merge }, 2\rangle & =\frac{m /\langle\text { merge }, 1\rangle}{m /\langle\text { merge }, 1\rangle} \\
m /\langle\text { merge }, 3\rangle & =\frac{1}{m}
\end{aligned}
$$

How to deal with them efficiently?

## Mode Graphs and Principal Mode Schemes

A set of mode constraints forms a "principal mode scheme" that can best be expressed as a mode graph.

$$
\begin{aligned}
m(\langle\text { merge }, 1\rangle) & =i n \\
m /\langle\text { merge }, 1\rangle\langle., 2\rangle & =m /\langle\text { merge }, 1\rangle \\
m /\langle\text { merge }, 2\rangle & =\frac{m /\langle\text { merge }, 1\rangle}{m /\langle\text { merge }, 1\rangle} \\
m /\langle\text { merge }, 3\rangle & =\frac{2>}{<m, 1\rangle}<
\end{aligned}
$$

A Stack \& Driver Example

```
drive(M,S):- M=:=0 | S= 1[].
drive(M,S):-M=\=0 | S=2[push(M),pop(N)|S1],
subtract(N,1,N1), drive(N1,S1).
stack([],
D) :- terminate(D).
stack([push(X)|S],D):- stack(S,p(X,D)).
stack([pop(X)|S], p(Y,D1)) :-
X=3Y, stack(S,D1).
```



Unification of Mode Graphs
..., stack(S, none), drive(10,S), ..

terminate (D).


```
qsort([], Ys0,Ys ):- Ys=Ys0.
qsort([X|Xs],Ys0,Ys3):-
    part(X,Xs,S,L),
    qsort(S,Ys0,[X|Ys2]), qsort(L,Ys2,Ys3).
part(_, [], S, L ):- S=[], L=[].
part(A,[X|Xs],SO,L ) :- A>=X |
    SO=[X|S], part(A,Xs,S,L).
part(A,[X|Xs],S, LO):-A< X |
    LO=[X|L], part(A,Xs,S,L).
```


driver(Fs,IOs0):-

IOs0=[gett(X)|IOs1], checkinput(Fs,IOs1,X). checkinput(Fs, IOs, done):-Fs=[], IOs=[]. checkinput(Fs0,IOs0,more) :-

Fs0 $=[\mathrm{N} \mid \mathrm{Fs} 1]$, IOs0 $=[\mathrm{putt}(\mathrm{N}), \mathrm{nl\mid IOs} 1]$, driver(Fs1,IOs1).


## Arrays

In Prolog, arg (and destructive set_arg) provides array access functionalities.

Principle: For built-in predicates, consider the mode constraints imposed by their virtual definitions.

Under the mode system, the basic access operation should be:

```
set_arg(I, TO, XO, X, T)
```




Moral: Values are resources. Array elements should be removed when accessed.

## Arrays

In Prolog, functor $(-,+,+)$ initializes the arguments of the created structure with distinct fresh variables, which are instantiated if necessary.

Under the mode system, the arguments should be initialized by constants, which are updated by set_arg/5.

- The update is in-place if the array occurs at a single-reference path.

Other generic array operations:

- swap
- split
- concatenate
- change-shape (for multidimensional arrays),


## Singleton Variables

A singleton variable may well be a misspelled variable.
cf. a "dangling" cable and an unplugged socket
$\mathrm{p}(\mathrm{XO}, \ldots):-\ldots, \mathrm{p}(\mathrm{XO}, \ldots)$.
$\rightarrow m /\langle\mathrm{p}, 1\rangle=I N \wedge m /\langle\mathrm{p}, 1\rangle=O U T$
$\rightarrow$ mode error

Note: Not all singleton variables indicate errors.

```
length([], NO,N):-N:=NO.
length([_|L],NO,N):-
    N1:=NO+1, length(L,N1,N).
```

- length will be used as a "byway" process without affecting the protocol of the rest of the processes.



## Well-Moded Programs Do Not Go Wrong

$P$ : a program
B: (body of) a goal clause
$m$ : a mode

## Lemma 1

If $B: m$ and $B$ contains $t_{1}=k t_{2}$, at least one of $t_{1}$ and $t_{2}$ is a variable.

## Theorem 1*

If $P: m, B: m$ and $P \vdash\langle B, \emptyset, V\rangle \longrightarrow\left\langle B^{\prime}, C^{\prime}, V^{\prime}\right\rangle$, then $C^{\prime}$ is consistent and $B^{\prime} \cdot C^{\prime}: m$.
( $B^{\prime} \cdot C^{\prime}: B^{\prime}$ instantiated by $C^{\prime}$ )

## Corollary 1*

If $P: m, B: m$ and $P \vdash\langle B, \emptyset, V\rangle \xrightarrow{*}\left\langle B^{\prime}, C^{\prime}, V^{\prime}\right\rangle$, then $C^{\prime}$ is consistent and $B^{\prime} \cdot C^{\prime}: m$.
*Holds unless the extended occur check (which excludes unification of the form $v=v$ ) fails.

## Extended Occur Check

A goal of the form $v=v$ creates a meaningless short-circuit.

The pair

$$
\begin{array}{ll}
G: & :-\mathrm{p}(\mathrm{~A}, \mathrm{~A}), \mathrm{q}(\mathrm{~A}), \mathrm{r}(\mathrm{~A}) . \\
P: & \mathrm{p}(\mathrm{X}, \mathrm{Y}):-\mathrm{X}=\mathrm{Y} .
\end{array}
$$

imposes $m /\langle\mathrm{q}, 1\rangle=m /\langle\mathrm{r}, 1\rangle=I N$, while the reduced goal

$$
G^{\prime}: \quad:-\mathrm{q}(\mathrm{~A}), \mathrm{r}(\mathrm{~A}) .
$$

imposes $m /\langle\mathrm{q}, 1\rangle=\overline{m /\langle\mathrm{r}, 1\rangle}$, which would violate Theorem 1.

## Groundness Property Follows from the Termination Property

## Theorem 2

Suppose $P$ : $m, B: m$, and $B$ has succeeded under the extended occur check. Then for each $v$ in $B$, a unification goal $\bar{v}=\stackrel{+}{t}$ or $\stackrel{+}{t}=\bar{v}$ must have been executed.

## Corollary 2

The final store maps all the variables in $B$ to ground terms.

## Cost of the Mode Analysis

- The number of constraints imposed: $O(n)$ ( $\forall$-quantified or non-quantified), where $n$ is the size of the program
- Adding one unary/binary constraint
$\approx$ unification of a feature graph with another small feature graph
$\approx$ merging of top-level features + merging of submode graphs
$\rightarrow \mathrm{O}(d \cdot \alpha(n))$ time, where
$d$ : size of the subgraph to be unified ('complexity' of data structures), $\alpha$ : inverse of the Ackermann function
- Total cost:

O( $n d \cdot \alpha(n)$ ) for all-at-once analysis $O(n \log n+n d \cdot \alpha(n)) \quad$ for separate analysis

## Non-Unary/Binary Constraints

- Imposed by non-linear clauses.
- Cannot be represented by mode graphs.
- Should be delayed - many of $n(>2)$-ary constraints will be reduced to unary/binary ones by other constraints.

$$
\begin{aligned}
& \mathrm{p}(\mathrm{X}, \ldots):-\mathrm{r}(\mathrm{X}, \ldots), \mathrm{p}(\mathrm{X}, \ldots) \\
& \rightarrow m /\langle\mathrm{r}, 1\rangle=I N \\
& m /\langle\mathrm{p}, 1\rangle=(\text { unconstrained })
\end{aligned}
$$

- Some constraints may remain unreduced, whose satisfiability must be checked eventually.
- The practical solution is to let programmers declare the modes of the paths where nonlinear variables occur.


## Implications to Programming Style

1. Advocates the "programming as wiring" paradigm, or (equivalently) programming with linear clauses.

- leads to more generic mode schemes
- encourages "structured dataflow"
- Iess error-prone

2. Encourages graceful termination.

- A process cannot discard its arguments upon termination if it contains variables to instantiate. (cf. Corollary 2)
(e.g., an output stream must be terminated by [].)
- All streams will be closed upon termination.


## Path-Based Program Analysis

## Path-based analysis can be used also for

- the distinction between one-to-one and possibly one-to-many communication
- which paths are 'shared' paths?
- type systems
- which paths are used for the data obeyed by the constraint system $\mathcal{C}$ ?
- what function symbols may appear at this path?

Resources received at linear (non-shared) paths in a clause head can be

- reclaimed (compile-time garbage collection) if not passed to the body, or
- locally recycled to represent new data used in the body,
without any run-time checking (e.g., reference counting).


## Resource-Conscious Programming

Insertion sort example:

```
sort([], S) :- S=[].
sort([X|LO],S) :-
    sort(LO,SO), insert(X,SO,S).
insert(X,[], R) :- R=[X].
insert(X,[Y|L], R) :- X=<Y | R=[X,Y|L].
insert(X,[Y|LO],R) :- X>Y |
    R=[Y|L], insert(X,LO,L).
```

By slight modification, becomes linear w.r.t. data resources:

```
sort([], S) :- S=[].
sort([X|LO],S) :-
    sort(LO,SO), insert([X],SO,S).
insert([X],[], R) :- R=[X].
insert([X],[Y|L], R) :- X=<Y | R=[X,Y|L].
insert([X],[Y|LO],R) :- X>Y |
    R=[Y|L], insert([X],LO,L).
```


## Extension: Polymorphic Modes

A polymorphic predicate is allowed to have different modes for each call.

Typical example: ‘=’ (unification)
For polymorphic predicates,

- compute their principal mode schemes (i.e., mode graphs), and
- allow different calls to have different instantiations of them. (e.g., merge ${ }_{1}$, merge $_{2}, \ldots$ )

This can be implemented by making a copy of the mode graph for each call, rather than the original graph.
(cf. ML: $(\lambda x . A) E$ vs. let $x=E$ in $A$ )

## Extension: Higher-Order

call is just a predicate with the constraint $m /\langle$ call, 1$\rangle=m$ (by confusing pred. and func. symbols).
apply needs extension.

```
twice(P,X,Z):- apply(P,X,Y), apply(P,Y,Z).
```

Whether P is

- a predicate symbol,
- a list of clauses (ground representation), or
- a compiled code with mode information,
it is a ground term at the first-order, but must have a predicate mode as well. The moding in the monomorphic case would be:




## Extension: Non-Herbrand Constraint Systems

- Rational terms - Immediate.
- Numerical constraints - Can be moded if dataflow can be determined statically.
- Equational theories
- Associativity and commutativity can be included naturally (they preserve resources). - Idempotency involves resource contraction/copying.

Example: Bags (= multisets) enjoy
$t_{1} \cup t_{2}=t_{2} \cup t_{1}$ and $t_{1} \cup\left(t_{2} \cup t_{3}\right)=\left(t_{1} \cup t_{2}\right) \cup t_{3}$.
So the paths for bags should obey the constraint:


## Constraint-Based Program Analysis

- Abstract interpretation usually computes fixpoints by iteration, while constraint-based analysis computes fixpoints by unification (or constraint solving)
- on the assumption that a single iteration should lead to a fixpoint.
- Constraint-based analysis provides unified treatment of
- declaration - constraints provided by a programmer,
- checking - consistency checking between constraints from the program and those given by programmers,
- inference - constraint solving.
- Incremental - inherently amenable to separate analysis.


## Related Work and Future

## Languages

- Strand (Foster and Taylor)
- Doc (Hirata)
- A'UM (Yoshida et al.)
- Moded Flat GHC (Ueda et al.)
- Janus (Saraswat et al.)


## Implementation of the Mode System

- MGTP-based (Koshimura et al.)
- Mode graph (Ueda)
- Another mode graph (Tick et al.)


## Applications and Future Work

- Message-oriented Implementation (sequential and parallel) (Ueda et al.)
- Optimized distributed unification
- Style checker
- High-performance computing

